

10/8/11, 4:22
update

EAST History			5/3/05	9:46 AM
BRS	817	tri-state adj bus	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	
BRS	829	tri adj state adj bus	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	
BRS	219	three adj state adj bus	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	
BRS	156	three-state adj bus	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	
BRS	1	three-statable adj bus	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	
BRS	1	three adj statable adj bus	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	
BRS	1040	S1 or S2 or S3 or S4 or S5 or S6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	
BRS	83	S7 and (interface adj logic)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	
BRS	83	S8 and (driver or driving or buffer)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	
BRS	71	S9 and (control adj signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	
IS&R	2903	(326/38-41).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	
BRS	818	tri-state adj bus	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	
BRS	830	tri adj state adj bus	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	
BRS	219	three adj state adj bus	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	
BRS	156	three-state adj bus	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	
BRS	1	three-statable adj bus	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	
BRS	1	three adj statable adj bus	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	

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BRS	1041	S12 or S13 or S14 or S15 or S16 or S17	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
BRS	34	S11 and S18	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB